

Trevor A. Brown

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Education

University of Toronto, Toronto, Canada, Jan. 2013 – Jun. 2017

Ph.D. Computer Science (supervised by Faith Ellen); A+ GPA

Thesis: Techniques for constructing efficient lock-free data structures (312 pages)

University of Toronto, Toronto, Canada, Sep. 2011 – Jan. 2013

M.Sc. Computer Science (supervised by Faith Ellen); A+ GPA

Thesis: Pragmatic primitives for non-blocking data structures

York University, First Class with Distinction, Toronto, Canada, 2006 – 2011

B.Sc. Hons. Major Computer Science, Hons. Minor Mathematics; A+ GPA

Research Interests

- Concurrent data structures
- Transactional memory
- Non-blocking algorithms
- Memory management
- Non-volatile memory
- Databases

Employment

Postdoctoral Researcher. IST Austria (Oct 2017 – present).

Working with Professor Dan Alistarh.

Postdoctoral Researcher. Technion, Israel Institute of Technology (Mar – Oct 2017).

Worked with Professor Hagit Attiya.

Research Intern. Oracle Labs East, Scalable Synchronization Group (Summer 2015).

Designed and implemented adaptive transactional lock-elision algorithms and work delegation algorithms for large scale systems with non-uniform memory architectures.

Web Application Developer. UPS Canada (2005 – 2006, 2008 – 2009).

Redesigned internal software to scale under a hugely increased user load.

Contributed over 200,000 lines of production code.

Conference and Journal Publications

Getting to the root of concurrent search tree performance.

Maya Arbel-Raviv, Trevor Brown and Adam Morrison.

Submitted.

Harnessing epoch-based reclamation for efficient range queries.

Maya Arbel-Raviv and Trevor Brown.

Submitted.

Reuse, don't recycle: transforming lock-free algorithms that throw away descriptors.

Maya Arbel-Raviv and Trevor Brown.

31st ACM Symposium on Distributed Computing (**DISC'17**), pp 4:(1-16).

Cost of concurrency in hybrid transactional memory.

Trevor Brown and Srivatsan Ravi.

31st ACM Symposium on Distributed Computing (**DISC'17**), pp 9:(1-16).

A template for implementing fast lock-free trees using HTM.

Trevor Brown.

36th ACM Symp. on the Principles of Distributed Computing (**PODC'17**), pp 293-302.

PHyTM: persistent hybrid transactional memory.

Hillel Avni and Trevor Brown.

Very Large Data Bases Journal Volume 10(4), 12 pages.

Full version of the paper at the VLDB'17.

PHyTM: persistent hybrid transactional memory.

Hillel Avni and Trevor Brown.

43rd International Conference on Very Large Data Bases (**VLDB'17**), pp 409-420.

Investigating the performance of hardware transactions on a multi-socket machine.

Trevor Brown, Alex Kogan, Yossi Lev and Victor Luchangco.

28th ACM Symp. on Parallelism in Algorithms and Architectures (**SPAA'16**), pp 121-132.

Reclaiming memory for lock-free data structures: there has to be a better way.

Trevor Brown.

34th ACM Symp. on the Principles of Distributed Computing (**PODC'15**), pp 261-270.

B-slack trees: space efficient B-trees.

Trevor Brown.

14th Scandinavian Symp. and Workshops on Algorithm Theory (**SWAT'14**), pp 122-133.

A general technique for non-blocking trees.

Trevor Brown, Faith Ellen and Eric Ruppert.

19th ACM Symp. on Princ. and Practice of Parallel Programming (**PPoPP'14**), pp 329-342.

Pragmatic primitives for non-blocking data structures.

Trevor Brown, Faith Ellen and Eric Ruppert.

31st ACM Symposium on the Principles of Distributed Computing (**PODC'13**), pp 13-22.

Range queries in non-blocking k-ary search trees.

Trevor Brown and Hillel Avni.

16th International Conf. on Principles of Distributed Systems (**OPODIS'12**), pp 31-45.

Non-blocking k-ary search trees.

Trevor Brown and Joanna Helga.

15th International Conf. on Principles of Distributed Systems (**OPODIS'11**), pp 207-221.

Workshop Papers

Cost of concurrency in hybrid transactional memory.

Trevor Brown and Srivatsan Ravi.

12th ACM SIGPLAN Workshop on Transactional Computing (**TRANSACT'17**), 8 pages.
Preliminary version of the conference paper at DISC'17.

Persistent hybrid transactional memory.

Hillel Avni and Trevor Brown.

11th ACM SIGPLAN Workshop on Transactional Computing (**TRANSACT'16**), 8 pages.
Preliminary version of the conference paper at VLDB'17.

Investigating the performance of hardware transactions on a multi-socket machine.

Trevor Brown, Alex Kogan, Yossi Lev and Victor Luchangco.

11th ACM SIGPLAN Workshop on Transactional Computing (**TRANSACT'16**), 8 pages.
Preliminary version of the conference paper at SPAA'16.

Short Papers and Posters

Reuse, don't recycle: transforming lock-free algorithms that throw away descriptors.

Maya Arbel-Raviv and Trevor Brown.

22nd ACM Symp. on Princ. and Practice of Parallel Programming (**PPoPP'17**), pp 429-430.
Poster and short version of conference paper at DISC'17.

Concurrent data structures.

Faith Ellen and Trevor Brown.

35th ACM Symp. on the Principles of Distributed Computing (**PODC'16**), pp 151-153.

Faster data structures in transactional memory using three paths.

Trevor Brown.

29th ACM Symposium on Distributed Computing (**DISC'15**), pp 671-672.
Short version of conference paper at PODC'17.

A general technique for non-blocking trees.

Trevor Brown, Faith Ellen and Eric Ruppert.

27th ACM Symposium on Distributed Computing (**DISC'13**), pp 567-568.
Short version of conference paper at PPoPP'14.

Articles and Technical Reports

A template for implementing fast lock-free trees using HTM.

Trevor Brown.

Arxiv Computing Research Repository (CoRR), abs/1708.04838, 20 pages.

Full version of conference paper at PODC'17.

Reuse, don't recycle: transforming lock-free algorithms that throw away descriptors.

Maya Arbel-Raviv and Trevor Brown.

Arxiv Computing Research Repository (CoRR), abs/1708.01797, 32 pages.

Full version of conference paper at DISC'17.

Non-blocking k-ary search trees.

Trevor Brown and Joanna Helga.

York University Technical Report CSE-2011-04, 52 pages.

Full version of conference paper at OPODIS'12.

Patent Applications

Adaptive techniques for improving performance of hardware transactions on

multi-socket machines. Oracle Labs, US patent application #20170075720, 34 pages.

Alex Kogan, Yossi Lev, Victor Luchangco and Trevor Brown.

Conference Talks and Invited Presentations

Good data structure experiments are R.A.R.E.

- Invited talk at the 1st Workshop on the Theory and Practice of Concurrency, Vienna, Austria (October 2017). *Held in conjunction with DISC'17.*

- Oath/Yahoo! Labs, Haifa, Israel (September 2017).

Reuse, don't recycle: transforming lock-free algorithms that throw away descriptors.

- 31st ACM Symposium on Distributed Computing, Vienna, Austria (October 2017).

Cost of concurrency in hybrid transactional memory.

- 31st ACM Symposium on Distributed Computing, Vienna, Austria (October 2017).

A template for implementing fast lock-free trees using HTM.

- 36th ACM Symposium on the Principles of Distributed Computing, Washington, United States (July 2017).

Techniques for Constructing Efficient Lock-free Data Structures.

- Technion - Israel Institute of Technology, Haifa, Israel (May 2017).

- University of Toronto, Toronto, Canada (March 2017).

Investigating the performance of hardware transactions on a multi-socket machine.

- 28th ACM Symposium on Parallelism in Algorithms and Architectures, Monterey, United States (July 2016).

- 11th ACM SIGPLAN Workshop on Transactional Computing, Barcelona, Spain (March 2016).

Persistent hybrid transactional memory.

- 11th ACM SIGPLAN Workshop on Transactional Computing, Barcelona, Spain (March 2016).

Faster data structures in transactional memory using three paths.

- 29th ACM Symposium on Distributed Computing, Tokyo, Japan (October 2015).

Scalable transactions on NUMA systems.

- Oracle Labs East, Burlington, United States (August 2015).

Reclaiming memory for lock-free data structures: there has to be a better way.

- 34th ACM Symposium on the Principles of Distributed Computing, San Sebastian, Spain (July 2015).

Hardware transactional memory and the lemming effect.

- University of Toronto, Toronto, Canada (April 2015).

Java Experiments on MTL: From past mistakes to best practices.

- York University, Toronto, Canada (March 2015).

Memory reclamation for lock-free data structures.

- University of Toronto, Toronto, Canada (August 2014).

B-slack trees: space efficient B-trees.

- 14th Scandinavian Symposium and Workshops on Algorithm Theory, Copenhagen, Denmark (June 2014).

A general technique for non-blocking trees.

- 19th ACM Symposium on Principles and Practice of Parallel Programming, Orlando, United States (February 2014).

- 27th ACM Symposium on Distributed Computing, Jerusalem, Israel (August 2013).

Pragmatic primitives for non-blocking data structures.

- 31st ACM Symposium on the Principles of Distributed Computing, Montreal, Canada (July 2013).

- University of Toronto, Toronto, Canada (July 2013).

Building a non-blocking chromatic tree.

- TransForm School on Research Directions in Distributed Computing, Heraklion, Crete (June 2013).

Range queries in non-blocking k-ary search trees.

- 16th International Conference on Principles of Distributed Systems, Rome, Italy (November 2012).

Non-blocking k-ary search trees.

- 15th International Conference on Principles of Distributed Systems, Toulouse, France (October 2011).

Experiences with Intel's Multicore Testing Lab.

- York University, Toronto, Canada (2011).

Supervisory Experience

(undergraduate research projects)

- Jialin Song: designing a locking scheme that bridges the gap between fine-grained and coarse-grained locking (University of Toronto, 2014).
- Ken Hoover: designing and implementing a non-blocking relaxed AVL tree, and a non-blocking relaxed (a,b)-tree (University of Toronto, 2013).

Teaching Experience

- *Teaching Assistant – University of Toronto* (2011 – 2014)
CSC2221: graduate theory of distributed computing (2013).
CSC263: data structures and analysis (2011, 2012, 2013, 2014).
CSC369: principles of operating systems (2012).
CSC265: enriched data structures and analysis (2011).
- *Programming contest coach – York University* (2009)
Held a seminar series on algorithms and problem solving. Gave short lectures, involving students in the process of solving algorithmic problems, then directed hands-on problem solving sessions, and held practice programming contests.

Software Artifacts

(<http://implementations.tbrown.pro>)

- **Java lock-free data structure library (22,000 lines of code)**
Produced the first lock-free unbalanced binary search tree implementation.
Produced the first implementation of LLX and SCX synchronization primitives.
Also introduced: k-ary search trees, relaxed AVL trees, Chromatic trees, b-slack trees.
Includes experimental test harness with support for 13 competing data structures.
- **C++ lock-free data structure library (23,000 lines of code)**
Produced the first C/C++ implementation of LLX and SCX synchronization primitives.
Implemented unbalanced BSTs and relaxed (a,b)-trees using LLX and SCX.
Produced four different transactional memory based algorithms for each data structure.
- **Lock-free memory reclamation in C++ (9,000 lines of code)**
Provided a record manager library with allocation, reclamation and object pooling plugins (including five allocators and four memory reclamation algorithms).
Implemented lock-free BSTs and Chromatic trees using this library.
- **Reusable descriptors for lock-free data structures in C++ (29,000 lines of code)**
Provided a lock-free reusable descriptor library.
Used this library to accelerate four advanced lock-free data structures.

- **Support for range query operations in C++ (55,000 lines of code)**
Produced three novel algorithms for adding range query operations to data structures. Augmented seven different data structures with range query support (producing up to five variants of each data structure). Used these data structures to accelerate an in-memory database (DBx1000).
- **PHyTM: non-volatile hybrid transactional memory for databases (proprietary code – Huawei Research)**
Implemented hybrid TM on a simulated system with non-volatile DRAM. Accelerated two in-memory databases by using PHyTM to synchronize threads (with large improvements over 2-phase locking and optimistic concurrency control).
- **Hardware lock elision for multsocket systems (proprietary code – Oracle Labs)**
Created a drop-in library for accelerating arbitrary lock-based code on systems with non-uniform memory architectures and hardware transactional memory. Demonstrated large speedups for data structures, the STAMP benchmarks, and real software such as the ccTSA gene sequencer, the paraheap-k clustering application and the Kyoto Cabinet database.

Academic Awards

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| NSERC Postdoctoral fellowship (\$90,000) Second highest ranked applicant | 2017 – 2018 |
| NSERC Alexander Graham Bell CGS D3 (\$105,000) | 2013 – 2016 |
| University of Toronto PhD fellowship (\$28,000) | 2013 – 2016 |
| NSERC Alexander Graham Bell CGS M (\$17,500) | 2011 |
| University of Toronto MSc fellowship (\$11,000) | 2011 |
| NSERC Undergraduate Research Awards (\$22,000) | 2009, 2010, 2011 |
| York University Entrance/Continuing Scholarships (\$3,720) | 2006 – 2010 |
| York Professor Ruth Hill Memorial Award (\$1,045) Awarded to the top undergraduate student in the Faculty of Science and Engineering | 2008 |

Other Achievements

- Award for Excellence in Teaching Assistance (U of T CS Student Union, 2014).
- Faculty of Science and Engineering Silver Medal (York University, 2011).
- Honourable Mention in the Computing Research Association's Outstanding Undergraduate Researcher Award competition (2011).
- Represented York at the ACM International Collegiate Programming Contests, ECNA region (2007 – 2011). In 2009, led senior school team to finish 6th (of 115).
- Dean's Honour Roll (York University) – Science and Engineering (2007 – 2011).
- Chair's Honour Roll (York University) – Mathematics and Statistics (2010).

Community Service

- **Program committee member** for ICDCS'18.
- **Conference paper reviews** for PODC'11, DISC'12, PODC'13, DISC'14, PODC'14, PPOPP'16, SIROCCO'16, SPAA'16, DISC'16, SPAA'17, RANDOM'17, DISC'17.
- **Artifact evaluation** for PPOPP'16.
- **Journal reviews** for the ACM Journal of Distributed Computing (DC/DIST), the IEEE Journal of Transactions on Parallel and Distributed Systems (TPDS), and the Elsevier Journal of Logical and Algebraic Methods in Programming (JLAMP).
- **Search committee for Chair of Computer Science**, University of Toronto (2015)
- **Wrote tenure recommendation letters**, York University
- **Developed student study resources** (2009 – 2010)
Developed online student resources for York courses in Operations Research and Japanese, and for would-be participants in the ACM ICPC programming contests. They have been accessed over 250,000 times.
- **Volunteer note-taker** (2008 – 2009)
Tutored and took notes on behalf of students with accessibility needs.